

NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics & Communication Engineering	Discipline: Engineering & Technology
Level : Under Graduate	Tier: 2
Application No: 11913	Date of Submission: 01-07-2026

PART A- Profile of the Institute

A1.Name of the Institute: AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY	
Year of Establishment : 2005	Location of the Institute: Near Tagarapuvalasa Bridge NH16
A2. Institute Address: CHERUKUPALLI VILAGE, NEAR TAGARAPUVALASA BRIDGE,CHITTIVALASA SO, BHOGAPURAM MANDAL, VIZIANAGRAM DIST,Andhra Pradesh,531162	
City:Vizianagaram	State:Andhra Pradesh
Pin Code:531162	Website:www.aietta.ac.in
Email:dean.academics@aietta.ac.in	Phone No(with STD Code):08933-226739
A3. Name and Address of the Affiliating University (if any):	
Name of the University : Jawaharlal Nehru Technological University-Gurajada	City: Vizianagaram
State : Andhra Pradesh	Pin Code: 535003
A4. Type of the Institution: Self-Supported Institute	
A5. Ownership Status: Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **6**
- No. of PG programs: **8**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Computer Application	PG	Master of Computer Application	2023	--	Computer Application
2	Engineering & Technology	PG	CAD/CAM	2012	--	Mechanical Engineering
3	Engineering & Technology	UG	Computer Science and Engineering	2005	--	Computer Science and Engineering
4	Engineering & Technology	PG	Computer Science and Engineering	2010	--	Computer Science and Engineering
5	Engineering & Technology	UG	Computer Science and Engineering (Artificial Intelligence & Machine Learning)	2020	--	Computer Science and Engineering
6	Engineering & Technology	PG	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	2025	--	Computer Science and Engineering
7	Engineering & Technology	UG	Computer Science and Engineering (Data Science)	2020	--	Computer Science and Engineering
8	Engineering & Technology	UG	Electrical & Electronics Engineering	2005	--	Electrical and Electronics Engineering
9	Engineering & Technology	UG	Electronics & Communication Engineering	2005	--	Electronics and Communication Engineering
10	Engineering & Technology	UG	Mechanical Engineering	2005	--	Mechanical Engineering
11	Engineering & Technology	PG	Power Electronics	2012	2025	Electrical and Electronics Engineering
12	Engineering & Technology	PG	Power Systems	2015	--	Electrical and Electronics Engineering

13	Engineering & Technology	PG	VLSI Design	2011	--	Electronics and Communication Engineering
14	Management	PG	Master of Business Administration	2008	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Mechanical Engineering	No	Mechanical Engineering	UG
Electrical and Electronics Engineering	No	Electrical & Electronics Engineering	UG
Electronics and Communication Engineering	No	Electronics & Communication Engineering	UG
Computer Science and Engineering	Yes	Computer Science and Engineering	UG
Computer Science and Engineering	Yes	Computer Science and Engineering (Artificial Intelligence & Machine Learning)	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information**B1. Provide the Required Information for the Program Applied For:**

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COI AUTHORITY APPROVAL
1	Electronics & Communication Engineering	UG	2005 / --	60	Yes	2025	240	2025	F.No. South 446421777;

Sanctioned Intake for Last Five Years for the Electronics & Communication Engineering	
Academic Year	Sanctioned Intake
2025-26	240
2024-25	180
2023-24	120
2022-23	120
2021-22	120
2020-21	120

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	ANANTHA RAO GONDU
B. Nature of appointment:	Regular
C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	240	180	120	120	120	120	120

N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	216	177	111	103	96	85	112
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	19	20	27	23	16	13
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	12	17	10	12	11	8	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	228	213	141	142	130	109	125

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	240	216	12	95.00
2024-25 (CAYm1)	180	177	17	107.78
2023-24 (CAYm2)	120	111	10	100.83

Average [(ER1 + ER2 + ER3) / 3] = 101.20≅ 100

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	132.00	132.00	132.00
B=No. of students who graduated from the program in the stipulated course duration	88.00	71.00	84.00
Success Rate (SR)= (B/A) * 100	66.67	53.79	63.64

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 61.37

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
X=(Mean of 1st year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 1st year/10)	7.83	7.51	7.17
Y=Total no. of successful students	145.00	51.00	63.00
Z=Total no. of students appeared in the examination	194.00	121.00	115.00
API [X*(Y/Z)]	5.85	3.17	3.93

Average API[(AP1+AP2+AP3)/3] : 4.32

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2nd year/10)	7.54	7.16	7.44
Y=Total no. of successful students	56.00	77.00	89.00
Z=Total no. of students appeared in the examination	71.00	91.00	96.00
API [X * (Y/Z)]	5.95	6.06	6.90

Average API [(AP1 + AP2 + AP3)/3] : 6.30

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.16	7.28	7.01
Y=Total no. of successful students	74.00	89.00	71.00
Z=Total no. of students appeared in the examination	77.00	89.00	76.00
API [X*(Y/Z)]:	6.88	7.28	6.55

Average API [(AP1 + AP2 + AP3)/3] : 6.90

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	132.00	132.00	132.00
X=No. of students placed	76.00	62.00	76.00
Y=No. of students admitted to higher studies	2.00	3.00	1.00
Z= No. of students taking up entrepreneurship	1.00	0.00	0.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	59.85	49.24	58.33

Average Placement Index = (P_1 + P_2 + P_3)/3: 55.81 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments**(Data to be filled in for the Department and Allied Departments)****C1. Faculty details of Department and Allied Departments**

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Natu Ascc (Reg Cont Ad h
1	ANANTHA RAO GONDU	XXXXXXXX89N	Ph.D	KONERU LAKSHMAIAH EDUCATION FOUNDATION	Electronics and Communication Engineering	23/01/2023	3.4	Associate Professor	Professor	01/07/2024	Regu
2	VENKATA RAMA KRISHNA TOTTEMPUDI	XXXXXXXX22F	Ph.D	JNTU KAKINADA	Electronics and Communication Engineering	10/05/2021	5.1	Professor	Professor		Regu
3	CH R PHANI KUMAR	XXXXXXXX27C	Ph.D	GITAM DEEMED UNIVERSITY	Electronics and Communication Engineering	13/12/2021	4.6	Professor	Professor		Regu
4	PUSARLA SANKARA RAO	XXXXXXXX04R	Ph.D	Sri Satya Sai University of technology & Medical Sciences Sehore	Electronics and Communication Engineering	01/06/2023	3	Assistant Professor	Associate Professor	20/03/2024	Regu
5	V SIVA BHASKARA RAO	XXXXXXXX56G	M.Tech	ANDHRA UNIVERSITY	RADAR AND MICRO WAVE ENGINEERING	13/06/2017	8.11	Assistant Professor	Assistant Professor		Regu
6	SYAMALA KANCHIMANI	XXXXXXXX68R	M.Tech	JNTU KAKINADA	VLSI System Design	09/01/2012	14.5	Assistant Professor	Assistant Professor		Regu
7	BODABALLA RAVI KIRAN	XXXXXXXX89G	M.Tech	ANDHRA UNIVERSITY	RADAR AND MICRO WAVE ENGINEERING	03/06/2024	2	Assistant Professor	Assistant Professor		Regu
8	SURIBABU KOYYA	XXXXXXXX89R	M.Tech	ANDHRA UNIVERSITY	Embedded Systems	11/11/2019	6.6	Assistant Professor	Assistant Professor		Regu
9	RAHUL DANTULURI	XXXXXXXX43M	M.Tech	JNTU KAKINADA	VLSI System Design	28/12/2020	5.5	Assistant Professor	Assistant Professor		Regu
10	URITI GEETA LAKSHMI	XXXXXXXX30C	M.Tech	JNTU KAKINADA	VLSI & Embedded System Design	17/04/2021	5.1	Assistant Professor	Assistant Professor		Regu

11	RAMBHA PADMA	XXXXXXX46N	M.Tech	JNTU KAKINADA	Digital Electronics and Communication Engineering	24/03/2021	5.2	Assistant Professor	Assistant Professor		Regu
12	LOKARANJANRAO PADALA	XXXXXXX26N	M.Tech	JNTU KAKINADA	VLSI System Design	13/07/2020	5.11	Assistant Professor	Assistant Professor		Regu
13	CHATLA VENKATA LAKSHMI	XXXXXXX77L	M.Tech	JNTU KAKINADA	VLSI System Design	06/04/2020	6.2	Assistant Professor	Assistant Professor		Regu
14	GANTEDA KRUSHNARAO	XXXXXXX44K	M.Tech	JNTU KAKINADA	VLSI & Embedded System Design	08/04/2020	6.2	Assistant Professor	Assistant Professor		Regu
15	BODDURU PRAVALLIKA	XXXXXXX78J	M.Tech	JNTU KAKINADA	Systems and Signal Processing	25/09/2021	4.8	Assistant Professor	Assistant Professor		Regu
16	SARASWATHI PATNANA	XXXXXXX18G	M.Tech	JNTU KAKINADA	VLSI System Design	19/06/2024	1.11	Assistant Professor	Assistant Professor		Regu
17	MEEGADA ANUSHA	XXXXXXX00G	M.Tech	JNTU HYDERABAD	Electronics and Communication Engineering	17/02/2022	4.3	Assistant Professor	Assistant Professor		Regu
18	ANEELKUMAR RONGALI	XXXXXXX75L	M.Tech	JNTU KAKINADA	VLSI System Design	12/02/2018	8.4	Assistant Professor	Assistant Professor		Regu
19	BONELA SUDHA	XXXXXXX84D	M.Tech	JNTU KAKINADA	Digital Electronics and Communication Engineering	21/03/2022	4.2	Assistant Professor	Assistant Professor		Regu
20	PONNANA PRABHAVATHI	XXXXXXX51L	M.Tech	JNTU KAKINADA	VLSI System Design	03/12/2018	7.6	Assistant Professor	Assistant Professor		Regu
21	GEETHA DUMPA	XXXXXXX14D	M.Tech	JNTU HYDERABAD	Embedded Systems	05/06/2023	3	Assistant Professor	Assistant Professor		Regu
22	KANCHUBOINA SWETHA	XXXXXXX79A	M.Tech	JNTU KAKINADA	VLSI Design	25/10/2019	6.7	Assistant Professor	Assistant Professor		Regu
23	MIDATHADA VINAY KUMAR	XXXXXXX36P	M.Tech	VIT University	Embedded Systems	20/03/2019	7.2	Assistant Professor	Assistant Professor		Regu
24	GUDLA BHANU GUPTA	XXXXXXX15M	M.Tech	JNTU KAKINADA	Digital Electronics and Communication Engineering	06/04/2022	4.2	Assistant Professor	Assistant Professor		Regu
25	PRATAP CHENNA	XXXXXXX31N	M.Tech	JNTU KAKINADA	VLSI & Embedded System Design	02/06/2025	1	Assistant Professor	Assistant Professor		Regu
26	B JAGDISHWAR RAO	XXXXXXX51P	M.Tech	JNTU KAKINADA	Digital Electronics and Communication Engineering	25/05/2023	3	Assistant Professor	Assistant Professor		Regu
27	VAMBARILLI RAMBABU	XXXXXXX65K	M.Tech	JNTU KAKINADA	Digital Electronics and Communication Engineering	05/06/2024	2	Assistant Professor	Assistant Professor		Regu
28	PULAMARASETTY SHYAMKUMAR	XXXXXXX89E	M.Tech	JNTU KAKINADA	VLSI Design	05/11/2018	7.7	Assistant Professor	Assistant Professor		Regu
29	KOTTA SATISH	XXXXXXX23F	M.Tech	JNTU KAKINADA	EMBEDDED SYSTEMS AND VLSI DESIGN	12/06/2025	0.11	Assistant Professor	Assistant Professor		Regu
30	BEERAKA ANJANEYULU	XXXXXXX25K	M.Tech	JNTU KAKINADA	VLSI Design	14/12/2022	1.4	Assistant Professor	Assistant Professor		Regu
31	BADE NARESH KUMAR	XXXXXXX30M	M.Tech	JNTU KAKINADA	EMBEDDED SYSTEMS AND VLSI DESIGN	02/06/2025	1	Assistant Professor	Assistant Professor		Regu
32	RONGALI SIVA	XXXXXXX62N	M.Tech	JNTU KAKINADA	EMBEDDED SYSTEMS AND VLSI DESIGN	06/02/2023	3.4	Assistant Professor	Assistant Professor		Regu
33	POTNURU BHAGYALAKSHMI	XXXXXXX90F	M.Tech	JNTU KAKINADA	VLSI	05/06/2024	2	Assistant Professor	Assistant Professor		Regu
34	BOLLAPRAGADA MOHANA RAJESH	XXXXXXX38G	M.Tech	GITAM DEEMED UNIVERSITY	Electronics Design and technology	10/06/2025	1	Assistant Professor	Assistant Professor		Regu

35	GONGADA ANUSHA	XXXXXXXX71A	M.Tech	JNTU KAKINADA	DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS	19/05/2022	1.10	Assistant Professor	Assistant Professor		Regu
36	SANTHOSHI BHAVANI SEELA	XXXXXXXX92L	M.Tech	JNTU KAKINADA	VLSI System Design	04/07/2017	7.9	Assistant Professor	Assistant Professor		Regu
37	SOWJANYA SURAMPALLI	XXXXXXXX46L	M.Tech	JNTU KAKINADA	VLSI System Design	14/02/2018	7.2	Assistant Professor	Assistant Professor		Regu
38	VAMSEEKRISHNA ALLAM	XXXXXXXX21H	Ph.D	KONERU LAKSHMAIAH EDUCATION FOUNDATION	Electronics and Communication Engineering	19/04/2021	5.2	Assistant Professor	Associate Professor	24/06/2023	Regu

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)

C= No. of Students in UG 3rd year (ST)

D= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year

B= No. of Students in PG 2nd year

Student Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	198	132	132
UG1.C	132	132	132
UG1.D	132	132	132
UG1: Electronics & Communication Engineering	462	396	396
PG1.A	18	18	18
PG1.B	18	18	18
PG1: VLSI Design	36	36	36
DS=Total no. of students in all UG and PG programs in the Department	498	432	432
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 498	S2= 432	S3= 432
DF=Total no. of faculty members in the Department	34	32	29
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 34	F2= 32	F3= 29
FF=The faculty members in F who have a 100% teaching load in the first-year courses	4	3	3
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 16.60	SFR2= 14.90	SFR3= 16.62
Average SFR for 3 years	SFR= 16.04		

C3. Faculty Qualification

- Faculty qualification index (FQI) = $2.5 * [(10X + 4Y)/RF]$ where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 * [(10X + 4Y) / RF]$
2025-26(CAY)	5	29	24.00	17.29

2024-25(CAYm1)	5	27	21.00	18.81
2023-24(CAYm2)	5	24	21.00	17.38

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = $1/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents.}$
- RF2= No. of Associate Professors required = $2/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- RF3= No. of Assistant Professors required = $6/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	3.00	3.00	6.00	2.00	17.00	29.00
2024-25	2.00	3.00	5.00	2.00	14.00	27.00
2023-24	2.00	2.00	5.00	2.00	14.00	25.00
Average	RF1=2.33	AF1=2.67	RF2=5.33	AF2=2.00	RF2=15.00	AF2=27.00

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	PSSN Patnaik	Project manager (Technical)	HMIES Solutions Pvt Ltd, Visakhapatnam, Andhra Pradesh	Design for Testing (VLSI)	55.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	M.Venkatesh	Design Engineer	HMIES Solutions Pvt Ltd, Visakhapatnam, Andhra Pradesh	ASIC Verification (VLSI)	60.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	PSSN Patnaik	Project manager (Technical)	HMIES Solutions Pvt Ltd, Visakhapatnam, Andhra Pradesh	Mastering PCB Design and Layout	52.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	17	7	3
2	No. of peer reviewed conference papers published	1	1	0
3	No. of books/book chapters published	0	5	0

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.G.Anantha Rao	Dr.P.Sankar Rao, D.Geetha	ECE	"Development of an Intelligent IoT-Based Industrial Safety Monitoring and Hazard Mitigation System."	Fluentgrid Limited (Rushikonda IT Hills)	1Year	2.60
Dr.P.Sankar Rao	Dr.P.Sankar Rao	ECE	"Development of an Efficient Transform-Domain Denoising and Enhancement Framework for Low-Dose Medical Images."	Fluentgrid Limited (Rushikonda IT Hills)	1Year	1.80
Dr.Ch.Raghavendra Phani kumar	P.Mohana Rajesh, Ch.venkata lakshmi	ECE	"Design and Implementation of a Dual-Network GSM and Wi-Fi Enabled Smart Fire and Gas Safety System."	Fluentgrid Limited (Rushikonda IT Hills)	1Year	2.10
						Amount received (Rs.):6.50

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.T.V.Rama Krishna	P.Lokaranjan Rao,K.Suribabu	ECE	"Design and Implementation of an Intelligent Smart Security System Featuring Real-Time Voice Authentication and Speech Recognition."	Cyient (Madhurawada IT SEZ):	1 Year	2.35
						Amount received (Rs.):2.35

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.T.V.Rama Krishna	R.Aneelkumar B.Pravallika	ECE	"Development of an IoT-Driven Context-Aware Simulation Framework for Real-Time System Analytics and Predictive Modeling."	Cyient (Madhurawada IT SEZ)	1 Year	2.70
Dr.Ch.Raghavendra Phani kumar	D.Rahul,V.Siva Bhaskar rao	ECE	"Development of a Machine Learning-Driven Framework for Mitigating Redundant Data Transmission in Communication Networks."	Cyient (Madhurawada IT SEZ)	1 Year	1.95
Dr.P.Sankar Rao	K.Syamala, M.Vinay kumar	ECE	"Design Automation of Area-Efficient VLSI Layouts for Connected and Pyramid Networks Using Deep Neural Learning."	Zen Technologies Limited (Hyderabad)	1 Year	2.30
						Amount received (Rs.):6.95

Total Amount (Lacs) Received for the Past 3 Years: 15.80**Note*:**

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.T.V.Rama krishna	Dr.Vamsee Krishna Allam,K.Syamala	R&D	Real-time Traffic Management using IoT-enabled Intelligent Transportation System integrated with ML	ANTAR IOT, Visakhapatnam	6 months	1.50
Dr.Raghavendra Phani kumar	M.Vinay Kumar, B.Sudha	R&D	Compact Wearable Antenna for IoT and Healthcare Applications	Lotus Wireless Technologies India Pvt. Ltd. – Auto Nagar, Visakhapatnam	9 months	2.30
						Amount received (Rs.):3.80

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Sankar Rao	M.Vinay Kumar, CH.Venkata Lakshmi	Dept of ECE	IoT-based Profiling Aquatic Diffusion System using Robotic Sensor Networks	INV Technologies	9 months	3.00
						Amount received (Rs.):3.00

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Sankar Rao	K.Syamala, B.Pravallika	Dept of ECE	AI based driver drowsiness detection system	Shri MK Embedded Solutions, Visakhapatnam	9 months	1.20
Dr.G.Ananth Rao	V.Siva Bhaskara Rao, D.Geetha	Dept of ECE	FPGA based real time image processing	OceanCore Technologies, Visakhapatnam	12 months	2.00
						Amount received (Rs.):3.20

Total amount (Lacs) received for the past 3 years: 10.00**Note*:**

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.G.Anantha Rao	Mobile Robot with Voice Interface	6 months	0.20	0.19	Prototype designed
Dr.Ch Raghavendra Phani kumar	Design of 5G mmWave Antenna for Smart City Application	1 year	0.20	0.19	Prototype designed
Dr.P.Sankar Rao	IoT-Based Environmental Monitoring and Alert System	6 months	0.20	0.18	Prototype developed
M.Vinay Kumar	AI-Based Real-Time Object Detection for Assistive Applications	8 months	0.20	0.20	Prototype developed
G.Bhanu Gupta	Low-Power VLSI Architecture for Edge-AI Applications	1 year	0.20	0.17	RTL Model developed
B.Jagdishwar Rao	Smart Agriculture Monitoring Using IoT and Wireless Sensors	6 months	0.20	0.19	Prototype developed
V.Rambabu	IoT-Based Smart Energy Monitoring System	6 months	0.20	0.19	Prototype developed
P.Shyamkumar	AI-Assisted MIMO Antenna Optimization for 5G Applications	1 year	0.20	0.17	Simulation and prototype completed
Dr.Vamsee Krishna Allam	Smart Wearable Assistive Device for Visually Impaired	1 year	0.20	0.19	Prototype designed
Dr.T.V.Rama Krishna	Deep Learning Based Real-Time Sign Language Recognition System	1 year	0.20	0.19	Prototype designed
Pratap Chenna	FPGA-Based Real-Time Image Processing System	8 months	0.20	0.18	Prototype developed
K.Satish	Embedded System for Industrial Equipment Monitoring	8 months	0.20	0.18	Prototype developed
			Amount received (Rs.): 2.40		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.Vamsee Krishna Allam	WiFi-Controlled Self-Balancing Robot	8 months	0.20	0.19	Prototype Demonstrated
Dr.T.V.Rama Krishna	Low-Power Retentive Flip-Flop for Energy-Efficient VLSI	1 year	0.20	0.19	RTL Design Completed
S.Santhoshi Bhavani	IoT-Based Disaster and Environmental Monitoring System	8 months	0.20	0.19	Prototype developed
S.Sowjanya	Implementation of Serial DAC Communication Using SPI in Verilog	6 months	0.20	0.19	RTL module verified
V.Siva Bhaskar Rao	Autonomous Disaster Response Drone with Embedded Vision	1 year	0.20	0.17	Prototype Demonstrated
K.Syamala	Coin-Based Mobile Charging Station with IoT Monitoring	6 months	0.20	0.19	Prototype developed
K.Suribabu	USB Port Security and Event Logging Framework	6 months	0.20	0.20	Prototype developed
Dr.G.Anantha Rao	Beamforming Optimization for mmWave 5G Communication	1 year	0.20	0.18	Simulation Model Developed
Dr.Ch Raghavendra Phani Kumar	Recognition Based Smart Attendance System	6 months	0.20	0.19	Prototype developed
B.Ravi Kiran	Automated Robotic Arm with Vision-Assisted Control	8 months	0.20	0.19	Prototype Demonstrated
D.Rahul	IoT-Based Water Quality Monitoring and Communication System	8 months	0.20	0.19	Prototype developed
			Amount received (Rs.): 2.20		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.G.Anantha Rao	Embedded Health Monitoring and Wireless Alert System	6 months	0.20	0.17	Prototype Developed
Dr.T.V.Rama Krishna	Compact Dual-Band Microstrip Antenna for IoT Applications	1 year	0.20	0.16	Antenna prototype fabricated
Dr.Ch Raghavendra Phani Kumar	FPGA-Based Digital Signal Processing Accelerator	8 months	0.20	0.18	FPGA prototype developed
P.Lokaranjan Rao	Real-Time Vehicle Tracking and Communication System	6 months	0.20	0.17	Prototype Developed
Ch.Venkatalakshmi	Image-Based Industrial Defect Detection Using Deep Learning	1 year	0.20	0.16	Prototype Developed
G.Krishna Rao	Secure IoT Communication and Lightweight Authentication	8 months	0.20	0.17	Prototype Demonstrated
B.Pravallika	Energy-Efficient Embedded System for Smart City Applications	1 year	0.20	0.16	Prototype Developed
Dr.P.Sankar Rao	AI-Based Speech and Signal Processing for Assistive Technology	8 months	0.20	0.18	Prototype Developed
U.Geetha Lakshmi	Smart Home Automation Using IoT and Embedded Systems	6 months	0.20	0.18	Prototype Developed
R.Padma	Low-Power IoT Sensor Node for Remote Monitoring	8 months	0.20	0.18	Prototype Developed
			Amount received (Rs.): 2.00		

Total amount (Lacs) received for the past 3 years : 6.60

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Electronic Devices and Integrated Circuits	36	1. Regulated Power Supplies 2. Cathode Ray Oscilloscopes 3. Function Generators 4. Digital	Semester 1 18	PUSARLA A	Lab Assistant	Diploma in EI
2	Micro Processor & Digital Electronics Laboratory	36	1. 8086 Micro Processor Kits 2. 8051 Micro Controller Kits 3. Key Board Module 4. Printer	Semester 1 36	MADHABATI	Lab Assistant	Diploma in EI
3	Communication Laboratory	36	1. Cathode Ray Oscilloscopes 2. Function Generators 3. AM & FM PCB Oscilloscope 4. Radio	Semester 2 30	TAMMINA JY	Lab Technician	B-TECH In E
4	Basic Electrical and Electronics Lab	66	1. Regulated Power Supplies 2. Cathode Ray Oscilloscopes 3. Function Generators 4. Digital	Semester 1	1.THALLA M	Lab Technician	B-TECH In E
5	Simulation Laboratory-I	36	1. No. Of Systems: 36 a) Processor : DUAL core b) Hard disk : 8GB RAM : 4GB	Semester 1 24	KALLA SANT	Lab Technician	B.TECH In EI
6	Simulation Laboratory-II	72	1.Number of System: 72 a) Processor : i5 9th Generation b) Hard Disk : 1TB c) RAM : 8GB	Semester 1 30	PEDDAMAJJ	Lab Technician	B.TECH In EI

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
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1	Electronic Devices and Integrated Circuits Laboratory	- Students must enter the laboratory only with the permission of the faculty. - Closed-toe shoes are mandatory - All experiments shall be performed only under the supervision of the laboratory instructor. - All labs displayed with basic Do's & Don'ts displayed to educate students. - Do not exceed the input voltage ratings of oscilloscopes and other measurement devices. - Observe the correct IC orientation before inserting it into the IC trainer kit or breadboard. - Fire extinguishers are available on each lab floor for emergency. - All the hardware labs are provided with First Aid Kit.
2	Micro Processor & Digital Electronics Laboratory	- Handle microprocessor kits, digital ICs, FPGA/Arduino boards, and other electronic components carefully. - Never insert or remove an IC while the power supply is ON. - Avoid applying excessive force while inserting ICs into sockets. - Closed-toe shoes are mandatory - All laboratories are provided with stabilizers to protect equipment from voltage fluctuations
3	Communication Laboratory	- Closed-toe shoes are mandatory - Students must strictly follow the laboratory manual and the instructions provided by the faculty - Never apply input voltages or signal levels beyond the specified limits of oscilloscopes, spectrum analyzers, or function generators. - Ensure proper ventilation to prevent overheating and maintain air quality - Ensure RF connectors (BNC, SMA, N-Type, etc.) are firmly connected before transmitting or measuring signals.
4	- Simulation Laboratory 1 - Simulation Laboratory 2	- Students are required to remove their footwear before entering the laboratory. - Verify that all computers, DSP trainer kits, and function generators are properly grounded before operation. - Ensure that all peripheral devices, including USB ports, serial interfaces, and JTAG connections, are correctly configured before use. - The use of external storage devices such as CDs, DVDs, pen drives, and external hard disks is strictly prohibited unless authorized by the faculty. - Properly shut down all computers and laboratory equipment after completing the experiment and before leaving the laboratory. - The laboratory is under continuous surveillance through CCTV cameras to ensure safety, security, and compliance with

D3. Project Laboratory/Research Laboratory

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PART E: First Year faculty and financial Resources**(Data to be filled in for the first year course faculty and budget allocation and utilization)****E1. First Year Student-Faculty Ratio (FYSFR)**

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) + (NS2*0.2))/RF
2023-24(CAYm2)	600	30	30	17	91
2024-25(CAYm1)	780	39	37	20	86
2025-26(CAY)	1200	60	47	19	69

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	68100000	67891010	57400000	57260503	39000000	38040795	25110000	24854502
Library	1871000	1782401	1805000	1706156	1750000	1662108	1744000	1495897
Laboratory equipment	1085000	934053	2260000	2154219	4275000	4070252	6942000	6732312
Teaching and non-teaching staff	126000000	100396670	118000000	98212662	105000000	97280900	85000000	74004123

Outreach Programs	116500	107402	107000	76638	97500	77660	87500	69041
R&D	1945000	1856220	2300000	2153307	2676000	2549873	1805200	1748154
Training, Placement and	7500000	7364611	5300000	5276294	5010000	5008593	1810000	1757126
SDGs	192000	182115	160000	152115	144000	136103	129000	122492
Entrepreneurship	134000	113828	114400	94334	87000	77210	74300	63156
Others, specify	21866000	25185210	18787800	16270943	16766150	20629995	14864800	19316436
Total	228809500	205813520	206234200	183357171	174805650	169533489	137566800	130163239

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	160000	113330	190000	147050	800000	674041	590000	505452
Software	1100000	995950	1080000	1003900	950000	861550	900000	814000
SDGs	130000	112155	100000	86150	80000	69258	65000	58350
Support for faculty development	25000	19500	22000	13500	20000	15000	15000	12525
R & D	420000	319560	470000	385080	320000	297196	420000	341005
Industrial Training, Industry expert,	95000	87230	90000	81050	85000	77690	70000	64150
Miscellaneous (guest lectures,	210000	184000	190000	172000	130000	114000	125000	110000
Total	2140000	1831725	2142000	1888730	2385000	2108735	2185000	1905482